OIPE
JUN 26 2006
PATENT & TRADEMARK OFFICE

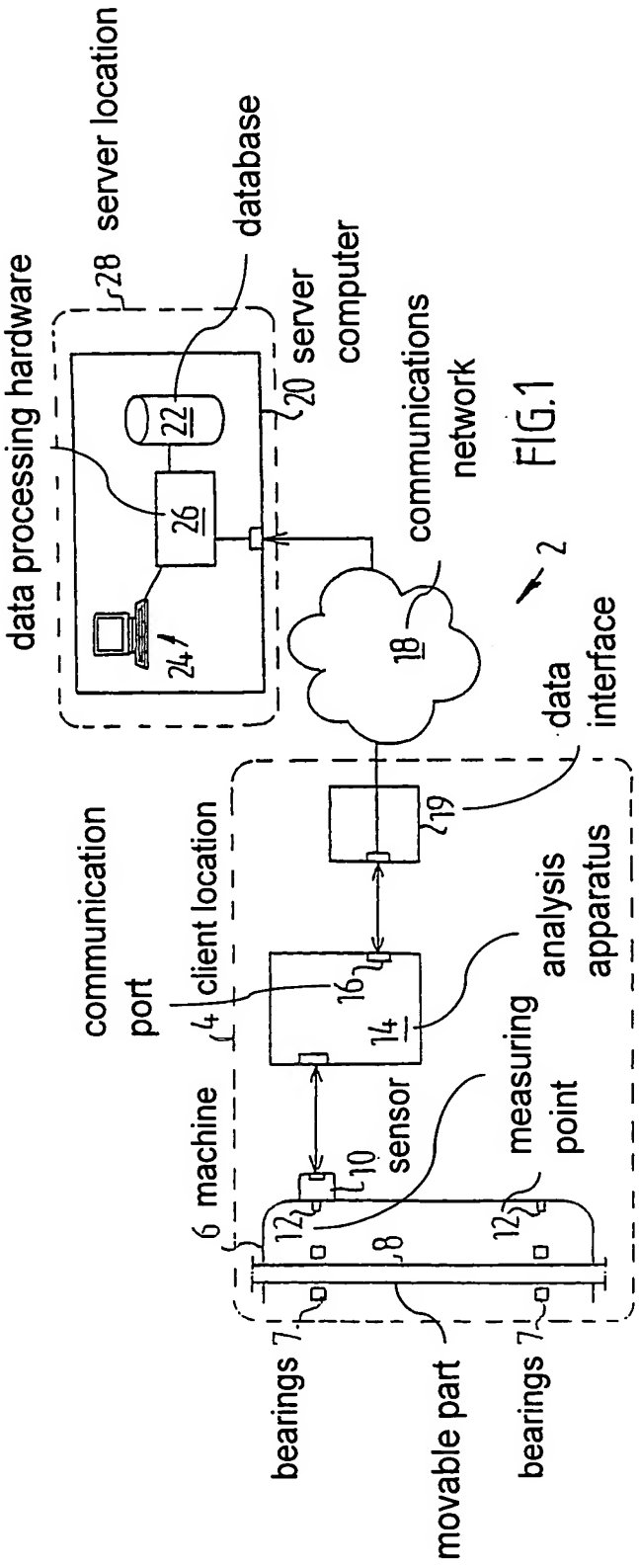

FIG.1

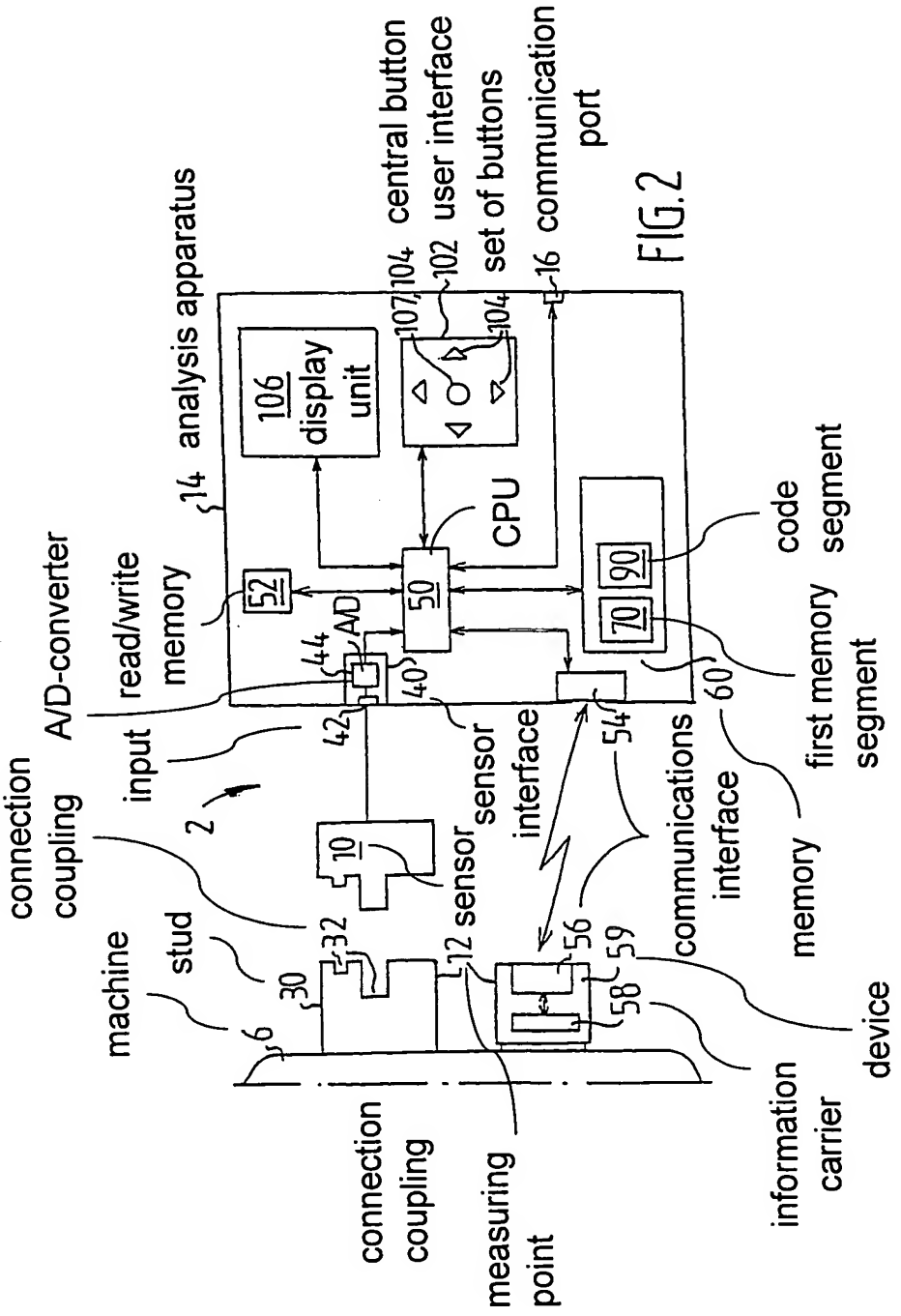
connection coupling
A/D-converter
input
read/write memory
14 analysis apparatus
central button
user interface
set of buttons
communication port
machine
stud
2
106 display unit
CPU
code segment
first memory segment
sensor
sensor interface
communications interface
memory device
connection coupling
measuring point
information carrier
AD
52
50
70
90
10
12
30
32
40
42
44
54
56
58
59
60
6
102
104
107
16

FIG.2

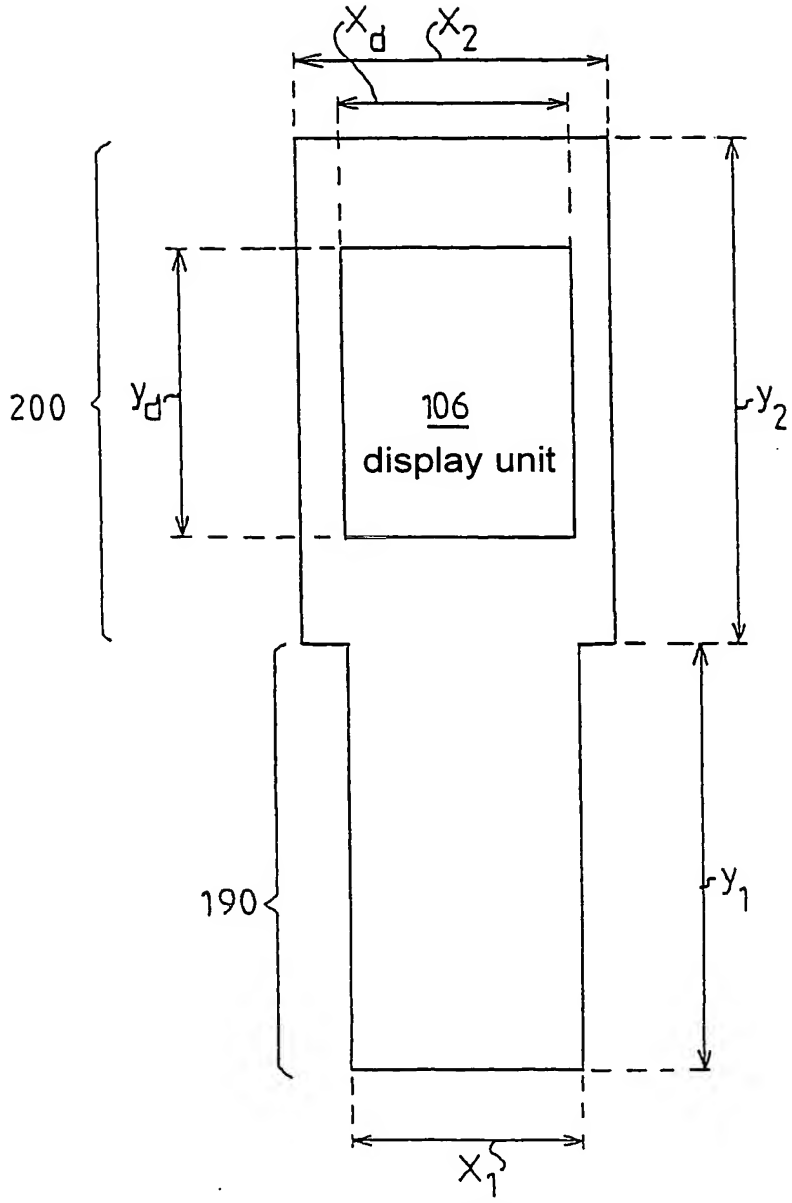

FIG.10

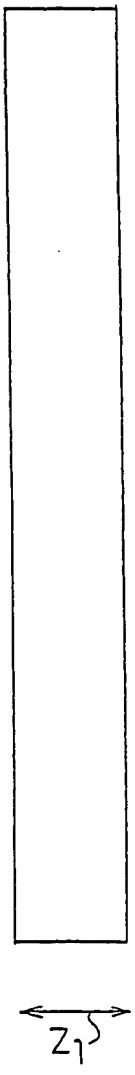

FIG.11

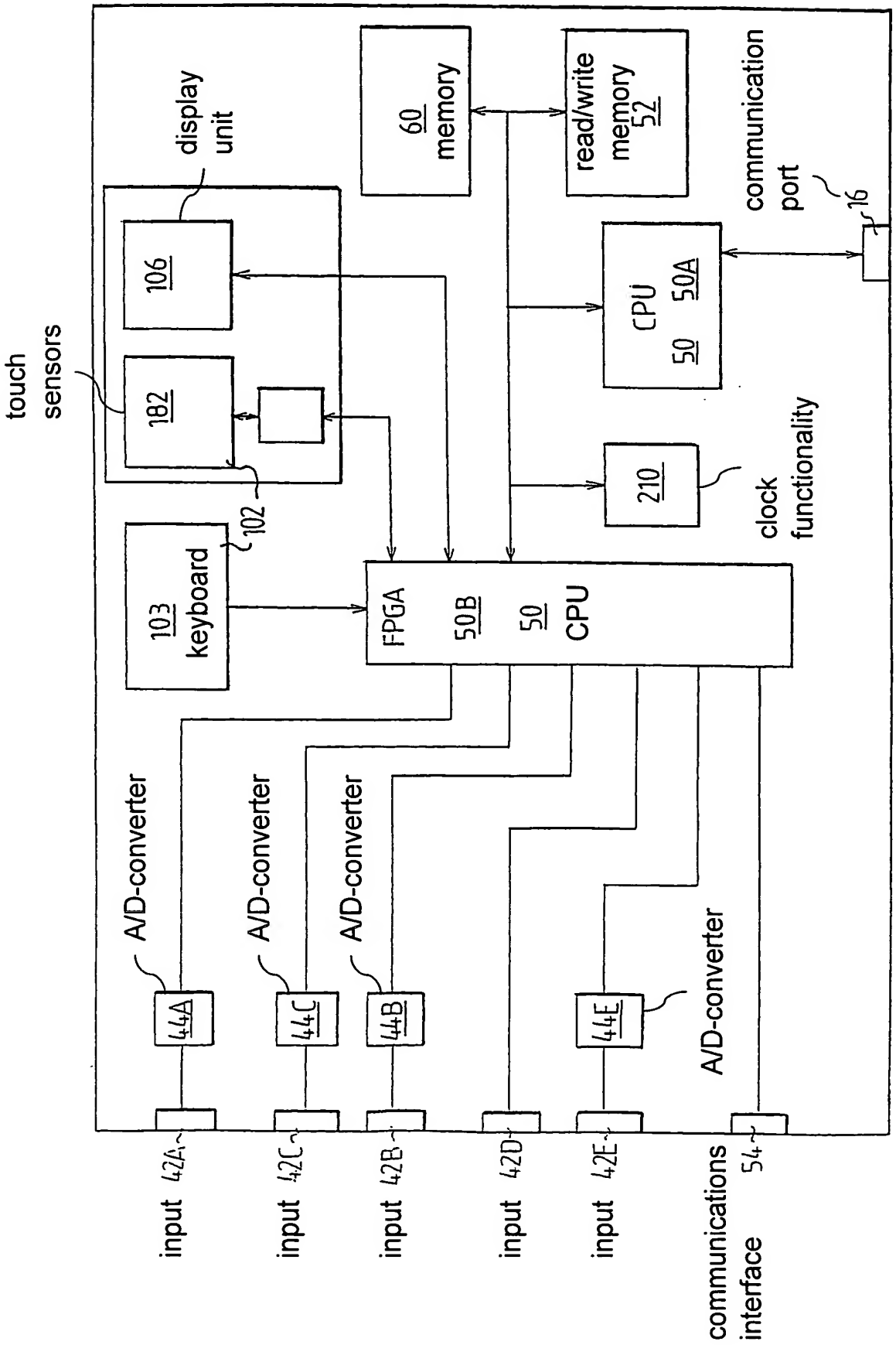
touch sensors
display unit
106
182
102
103 keyboard
60 memory
read/write memory 52
communication port
16
CPU 50 50A
210
clock functionality
FPGA 50B
50 CPU
A/D-converter
44A
44C
44B
44E
42A
42C
42B
42D
42E
input
communications interface
54
analysis apparatus 14

FIG. 12

To FIG. 14A

From FIG. 14A

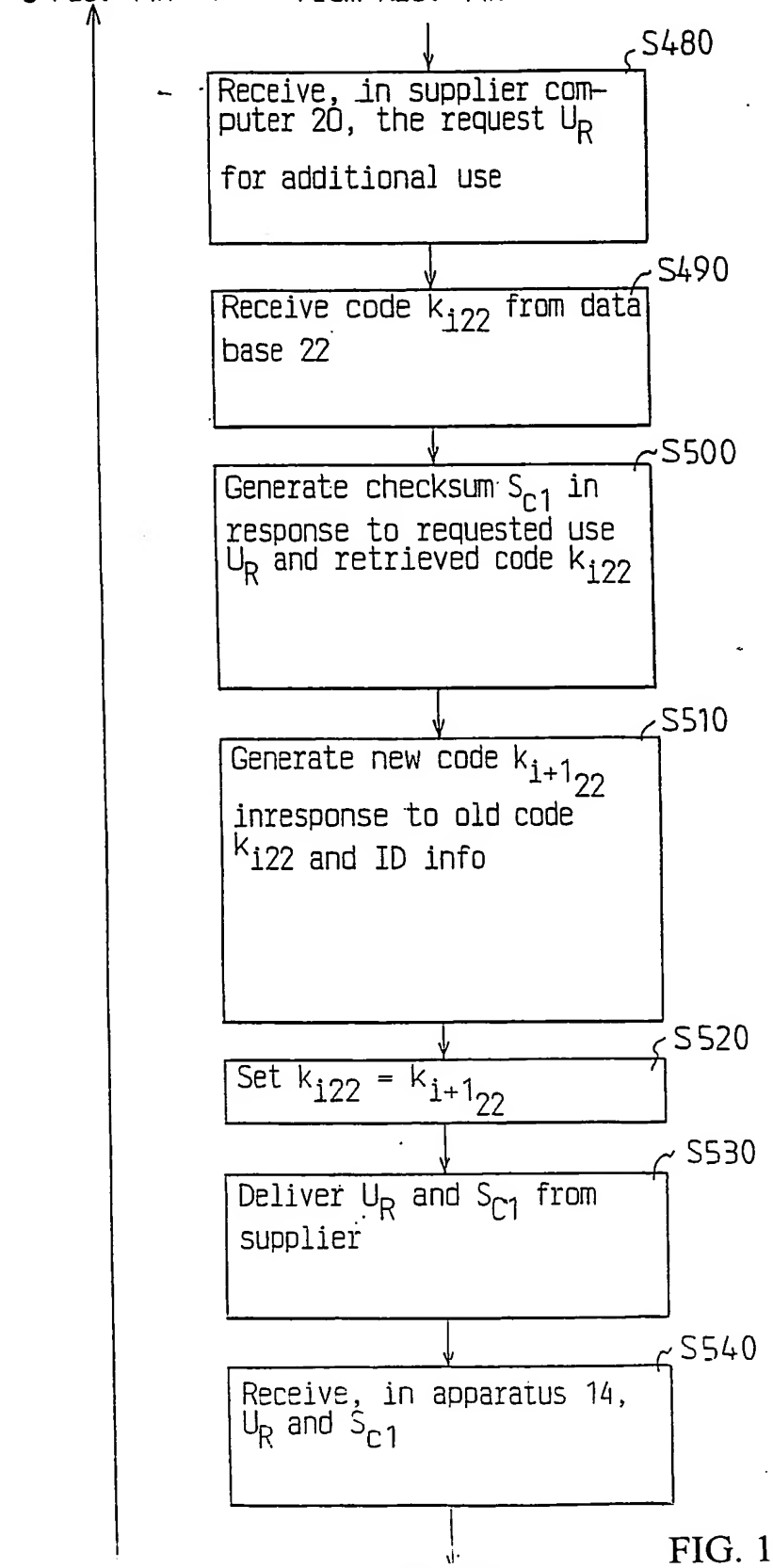

FIG. 14B

From FIG. 14 C

To FIG. 14C

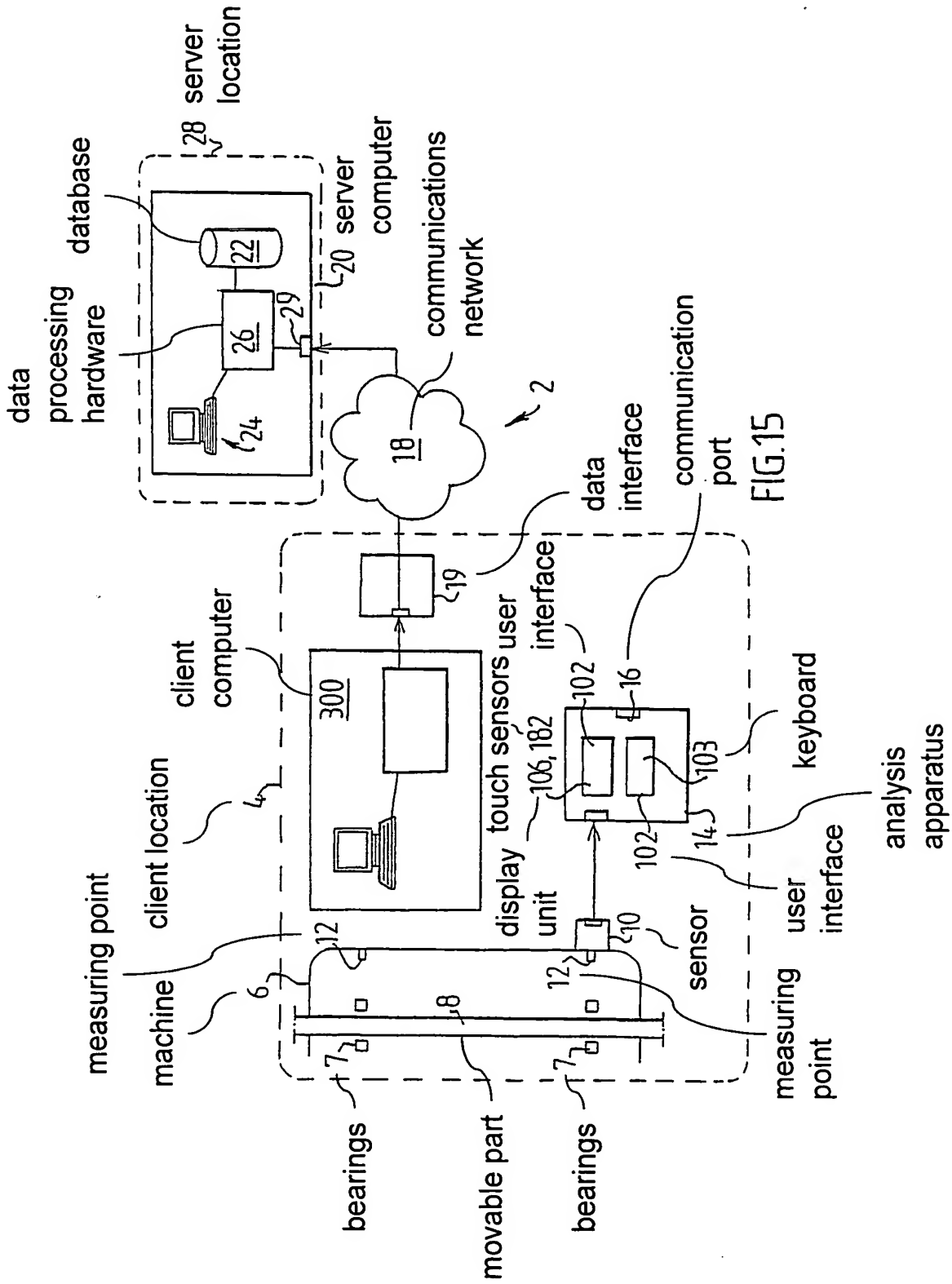
server location
28
database
data processing hardware
22
26
29
24
server computer
20
communications network
18
2
data interface
communication port
19
client computer
300
client location
4
measuring point
machine
6
12
7
8
bearings
movable part
bearings
display unit
touch sensors
106, 182
user interface
102
16
keyboard
103
14
sensor
10
measuring point
user interface
analysis apparatus

FIG.15